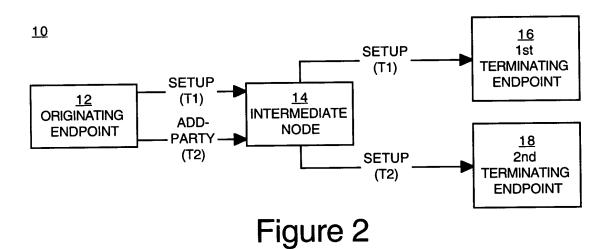


Figure 1



32
PROCESSOR

INPUT/OUTPUT
DEVICE

DOWNSTREAM
NODE(S)

DOWNSTREAM
NODE(S)

Figure 3

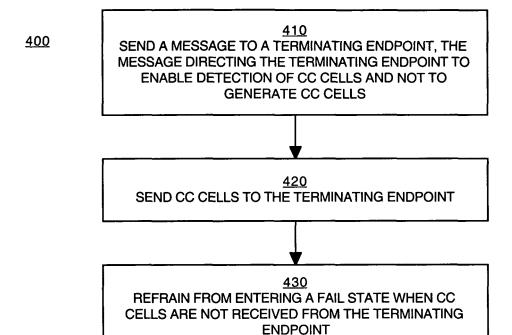


Figure 4

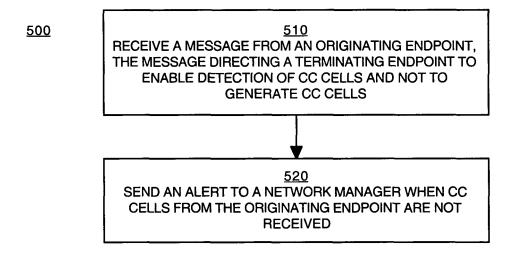


Figure 5